

### **AMENDMENTS TO THE SPECIFICATION**

**Please amend the specification to read as follows:**

Page 12, amend the last paragraph as follows:

In the steps illustrated at Figure 10, the 64 data points for each of the 8 and 16 Hz signals as acquired by the process steps shown in Figure 9 are processed. First, at steps 120, 122, 124, mean, peak and counter registers ~~peak~~ are cleared. Then, for the first data point acquired, the multiplication of the absolute value of the 8 Hz signal together with the 16 Hz signal is computed, at step 126, to which a mean figure, comprising a previous mean, plus the result of step 126 is computed, this being executed at step 128. After this, at step 130, comparison of the absolute value of the result of step 126 is made with that of a peak value (initially zero) and if that absolute value is greater than the peak, the peak is, at step 132, updated to reflect the absolute value of the result of step 126. Next, a counter initially set to zero is incremented one step, this being effected at step 134. Then, at step 136, it is determined whether the counter has reached a stored count of 64. If it has not, steps 126 through 136 are repeated, this repeating being effected until the count reaches 64 after which at step 138 there is computed a mean value representing the mean accumulated by the repeated executions of step 128 divided by 64. After that, at step 140, signal processing is judged complete.